# HITACHI

FOR	MESSRS:	•

DATE: May.13,2008

# CUSTOMER'S ACCEPTANCE SPECIFICATIONS SP14N01L6VLCA

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\* When products will be discontinued, customers will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY;		PROPOSED BY:	Dan (	Long
KAOHSIUNG HITACHI	Sh.	7B64PS 2701- SP14N01L6VLCA-4	PAGE	1 1/1
ELECTRONICS CO.,LTD.	No.	7504F3 270T- 3F (4NOTEOVECA-4	PAGE	1-1/10

## RECORD OF REVISION

DATE	SHEET No.	SUMMARY							
Jun.17,'05	7B64PS 2705 –	5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT							
	SP14N01L6VLCA-2 PAGE 5 – 1/1	Changed  VLED(+) O  VLED(-) O  VLED(-) O  VLED(-) O							
May.28,'07	7B64PS 2704- SP14N01L6VLCA-3 Page 4-1/1	4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS Added Operating Life: (40,000h)							
	7B64PS 2705- SP14N01L6VLCA-3 Page 5-1/1	5.2 ELECTRICAL CHARACTERISTICS OF LED BACKLIGHT Revised							
	rage 5-1/1	ITEM TYP. MAX.							
		Power Supply Current for LED 130 140							
	1	<b>↓</b>							
		ITEM TYP. MAX.							
		Power Supply Current for LED 80 90							
		Ambient Temperature vs. Allowable Forward Current  40  20  10  20  Ambient Temperature Ta(C)  Ambient Temperature Ta(C)							
	7B64PS 2706- SP14N01L6VLCA-3 Page 6-3/3	6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT Revised:							
·	. 290 0 0,0	ITEM MIN. TYP. NOTE							
		Brightness 90 115 ILED=130mA							
		<b>↓</b>							
		ITEM MIN. TYP. NOTE							
		Brightness   140   170   ILED=(80)mA							
KAOHSIUN ELECTRON	G HITACHI ICS CO.,LTD.	E May 13,'08 Sh. No. 7864PS 2702-SP14N01L6VLCA-4 PAGE 2-1/2							

## RECORD OF REVISION

DATE	SHEÉT No.			<del></del>		SUMMARY					
May.28,'07	7B64PS 2709-		3 Int	ernal Pin	Conne						
, ,	SP14N01L6VLC	A-3 Ch									
	Page 9-3/3	CF	CFL I/F: Mitsumi M63M83 $-$ 04 $\rightarrow$ JAE IL-G-4S-S3C2-S								
	7B64PS 2712-	12	12. DESIGNATION OF LOT MARK								
	SP14N01L6VLC	A-3 Ac									
	Page 12-1/1			REV No	).	ITEM		LOT No.			
						CFL I/F Connector					
				_		itsumi M63M83 - (					
						L I/F Connector :		<del></del>			
				A		AE IL-G-4S-S3C2	SV	7102T			
	•			_ ^			ľ	11021			
						perating Life (40,00	ן (ווטכ				
May.13,'08	7B64PS 2714-				ING C	ONDITIONS					
	SP14N01L6VLC	4-4  Ch	nange					10			
	Page 14-1/3			ITEM		SPECIF		18			
		6	Actuati	on Force	<u>l</u>	(10-	-50g)				
		.   -				Ψ			<del> </del>		
				ITEM		SPECIFICATIONS			•		
	•	[	Actuation Force 1.2N max.								
		1.4	2 1	INDI IT N	/ETHO	D & ACTUATIO	NI EO	DCE			
		E .	14.2.1 INPUT METHOD & ACTUATION FORCE Changed:								
			Г	ITEM		SDECIFICATION A		NOTE			
•						SPECIFICATION		NOTE			
				PE	N	(10~50g)	R0.8,	Polyacetal	pen		
	•		Г		<del></del>						
			_	ITE	M	SPECIFICATION		NOTE			
				PE	N	1.2N max.	R0.8,	Polyacetal	pen		
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AOHSIUNG	HITACHI	ATE Ma	ay.13	, Sh.	70640	S 2702-SP14N01L6	vá o i d	PAGE	2-2/2		
	11.37	7 I F 1 W//		יאטו			1371. C · N · M		/_/!		

## 3. GENERAL SPECIFICATIONS

(13) DC/DC Circuit

(1) Part Name SP14N01L6VLCA 159.4(W)mm x 101.0(H)mm x 12.8(D)mm (max.) (2) Outer Dimensions (3) Viewing Area 123 mm min. x 68 mm min. (4) Dot Size 0.48(W)min. x 0.48(H)min. (5) Dot Pitch 0.50(W)mm x 0.50(H)mm (6) Dot Number (Resolution) 240 (W) x 128 (H) (7) Duty Ratio 1/128 (8) LCD Type Transmissive type F-STN With anti-glare type upper polarizer (9) Viewing Direction 6 O'clock (10) Back Light Type LED (Color: White). (11) Touch Panel Analog resistive Transparency: 76% min. Surface Type: Anti glare T6963C / Toshiba (12) LCD Controller

**BUILT-IN** 

#### 4. ABSOLUTE MAXIMUM RATINGS

#### 4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

VSS=0V:STANDARD

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply For Logic	VDD-VSS	0	7.0	V	
Input Signal Voltage	Vi	-0.3	VDD+0.3	V	(Note 1)
Input Signal Current	li	0	1	Α	,
Static Electricity	VESD0	-	±100	V	(Note 1,2,3)
	VESD1	_	±10	kV	(Note 1,2,4)

Note 1: Make certain you are grounded when handling LCM.

Note 2 : Energy storage capacitance 200pF , discharge resistance 250 Ω Ta=25 °C , 60%RH.

Note 3: Contact discharge to I/F connector pins.

Note 4: Contact discharge to front metal bezel.

#### 4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STO	RAGE	COMMENT
	MIN.	MAX.	MIN.	MAX.	·
Ambient Temperature	-10°C	<b>60</b> ℃	<b>-20</b> ℃	<b>70</b> ℃	(Note 2,3)
Humidity	(Not	e 1)	(No	te 1)	without condensation
	,	2.45m/s <sup>2</sup>		11.76m/s <sup>2</sup>	
Vibration	-	(0.25G)	-	(1.2G)	(Note 4)
				(Note 5)	1h max.
		29.4m/s <sup>2</sup>		490.0m/s <sup>2</sup>	·
Shock	_	(3 G)	_	(50 G)	XYZ directions
				(Note 5)	
Corrosive Gas	Not Accep	table	Not Acceptable		
Operating Life	(40,0	00 h)			At 25℃ , I <sub>LED</sub> =80mA
(Note 7)		(Note 6)		<b>-</b>	max.

Note 1 : Ta ≤ 40°C : 85%RH max.

Ta> $40^{\circ}$ C: Absolute humidity must be lower than the humidity of 85%RH at  $40^{\circ}$ C

Note 2 : Ta at  $-20^{\circ}$ C < 48h, at  $70^{\circ}$ C < 168h.

Note 3: Background color changes slightly depending on ambient temperature.

This phenomenon is reversible.

Note 4:5Hz~100Hz (except resonance frequency)

Note 5: This module should be operated normally after finishing the test.

Note 6: When brightness reached 50% of initial brightness.

Note 7: Life time is estimated data.

KAOHSIUNG HITACHI	DATE	May 13 '00 S	h. 750450 0704 0714 071 071 071	DAGE	- 4 4 /4
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## 5. ELECTRICAL CHARACTERISTICS

#### 5.1 ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage For Logic	VDD-VSS	-	4.75	5.0	5.25	V
Input Signal Voltage	Vi	H LEVEL	0.8VDD	_	VDD	V
•		L LEVEL	0	_	0.2VDD	V
Power Supply Current For Logic (Note 1)	IDD	VDD-VSS=5.0V	-	(40)	_	mA
Recommended		Ta= $0^{\circ}$ C , $\phi = 0^{\circ}$	-	(16.9)	_	V
LC Driving Voltage	VDD-V0	Ta=25°C , <i>φ</i> =0°	-	(15.8)	-	V
(Note 2,3)		Ta=50°C , <i>φ</i> =0°	_	(15.2)	-	V

Note 1: Test pattern is all "Q", VDD-V0=(15.8)V, Ta=25°C

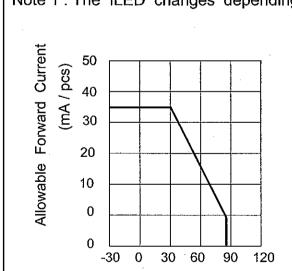
Note 2 : Recommended LC driving voltage may fluctuate about ±1.0V by each module test pattern is all "Q".

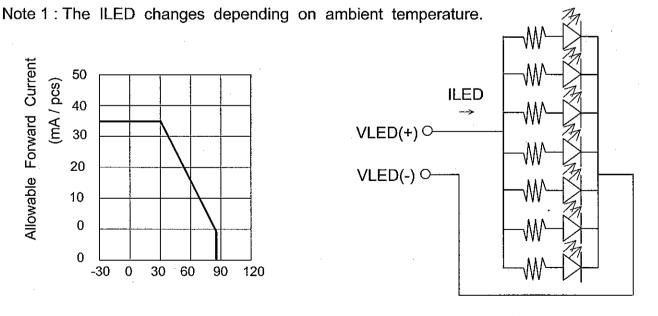
Note 3: LC Driving voltage depend on the value of resistant between R<sub>VR1</sub> and R<sub>VR2</sub>.

#### 5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

Ta=25°C

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage for LED	VLED	<u>.</u>	4.8	5.0	5.2	٧
Power Supply Current for LED	ILED	VLED=5.0V	-	80	90	mA





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## 6. OPTICAL CHARACTERISTICS

## 6.1 OPTICAL CHARACTERISTICS OF LCD

Ta=25°C (Backlight On)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE	
Viewing Area	$\theta$	K≧2.0	_	90		deg	1	
Viewing Area	ф	N <u>⊆</u> <b>Z</b> .0		80		dog	•	
Contrast Ratio	К	φ=0°, θ=0°	-	20	-	_	2	
Response Time (Rise)	tr	φ=0°, θ=0°	-	(330)	-	ms	3 /	
Response Time (Fall)	tf	φ=0°, θ=0°	. –	(150)	_	ms	3	

(Measure condition by HITACHI)

Note1. Definition of Viewing Angle

R-L Direction

F-R Direction

REAR

12:00

RIGHT

3:00

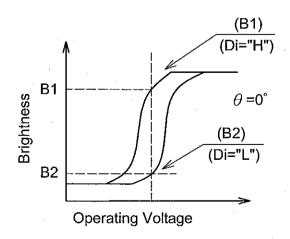
\*For This Product

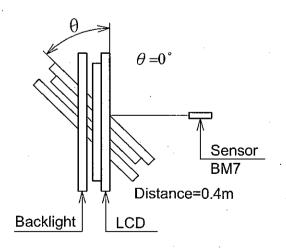
\*For This Product
The Viewing Direction is 6 O'clock ( $\theta$  a >  $\theta$  b)  $\theta = \theta$  a +  $\theta$  b ;  $\phi = \phi$ a+ $\phi$ b

Note2. Definition of contrast"K"

K= Brightness on selected dot (B1)

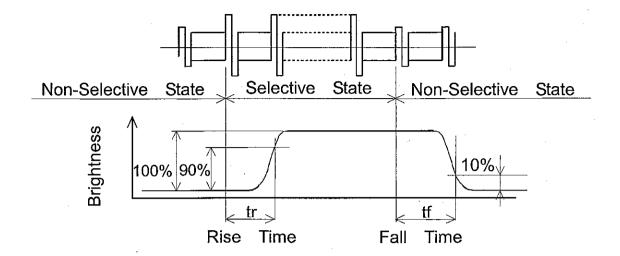
Brightness on non-selected dot (B2)





KAOHSIUNG HITACHI	DATE	May 12 '00	Sh.	7P64D9 2706 SD44N04L6VLCA 4	DAGE	0 4/0	١
ELECTRONICS CO.,LTD.	DATE	Way. 13, 06	No.	7B64PS 2706-SP14N01L6VLCA-4	PAGE	0-1/3	

Note 3: Definition of optical response



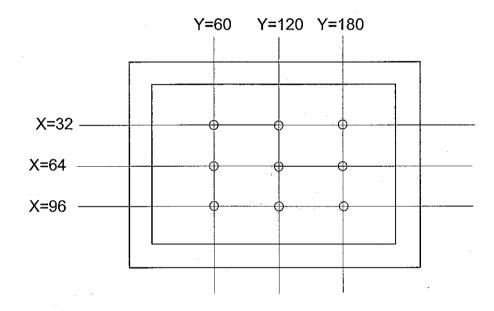
#### 6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness	140	170	-	cd/m²	ILED=(80)mA
Brightness Uniformity	· <u>-</u>	<del>-</del>	±35	%	(Note 1,)

Ta=25°C, Display data should be all "ON".

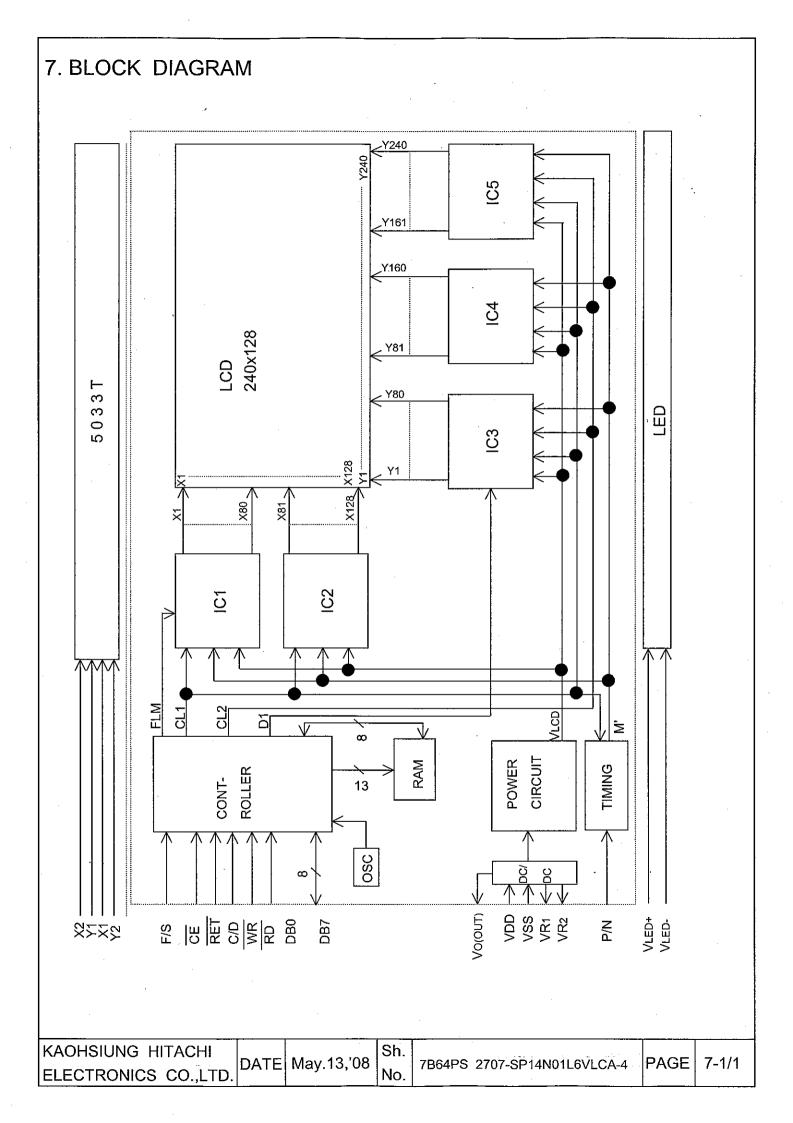
The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.

Note 1: Measure of the following 9 places on the display.



Definition of the brightness tolerance.

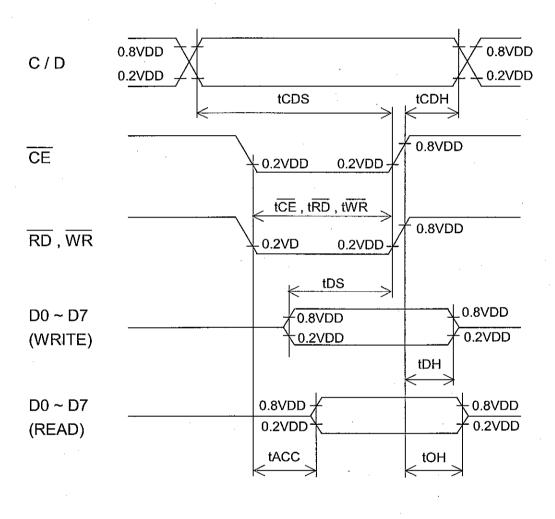
KAOHSIUNG HITACHI	DATE	Marri 40 200	Sh.		D 4 C L	C 0/0
ELECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B64PS 2706-SP14N01L6VLCA-4	PAGE	6-3/3



## 8. INTERFACE TIMING

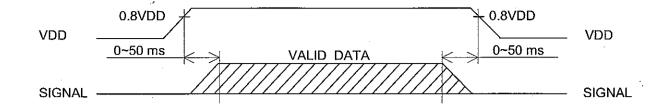
## 8.1 INTERFACE TIMING

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
C / D Setup Time	tCDS	100	-	_	ns
C / D Hold Time	tCHD	10	-	_	ns
CE, RD, WR Pulse Width	tCE, tRD, tWR	80	-	-	ns
Data Setup Time	tDS	80	-	-	ns.
Data Hold Time	tDH	40	-	-	ns
Access Time	tACC	-	-	150	ns
Output Hold Time	tOH	10	-	50	ns

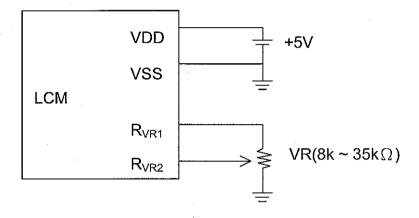


KAOHSIUNG HITACHI		May 12 '09	Sh.	7D04D0 0700 004 4H04L0% 04 4	DACE	0.4/0
ELECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B64PS 2708-SP14N01L6VLCA-4	PAGE	0-1/2

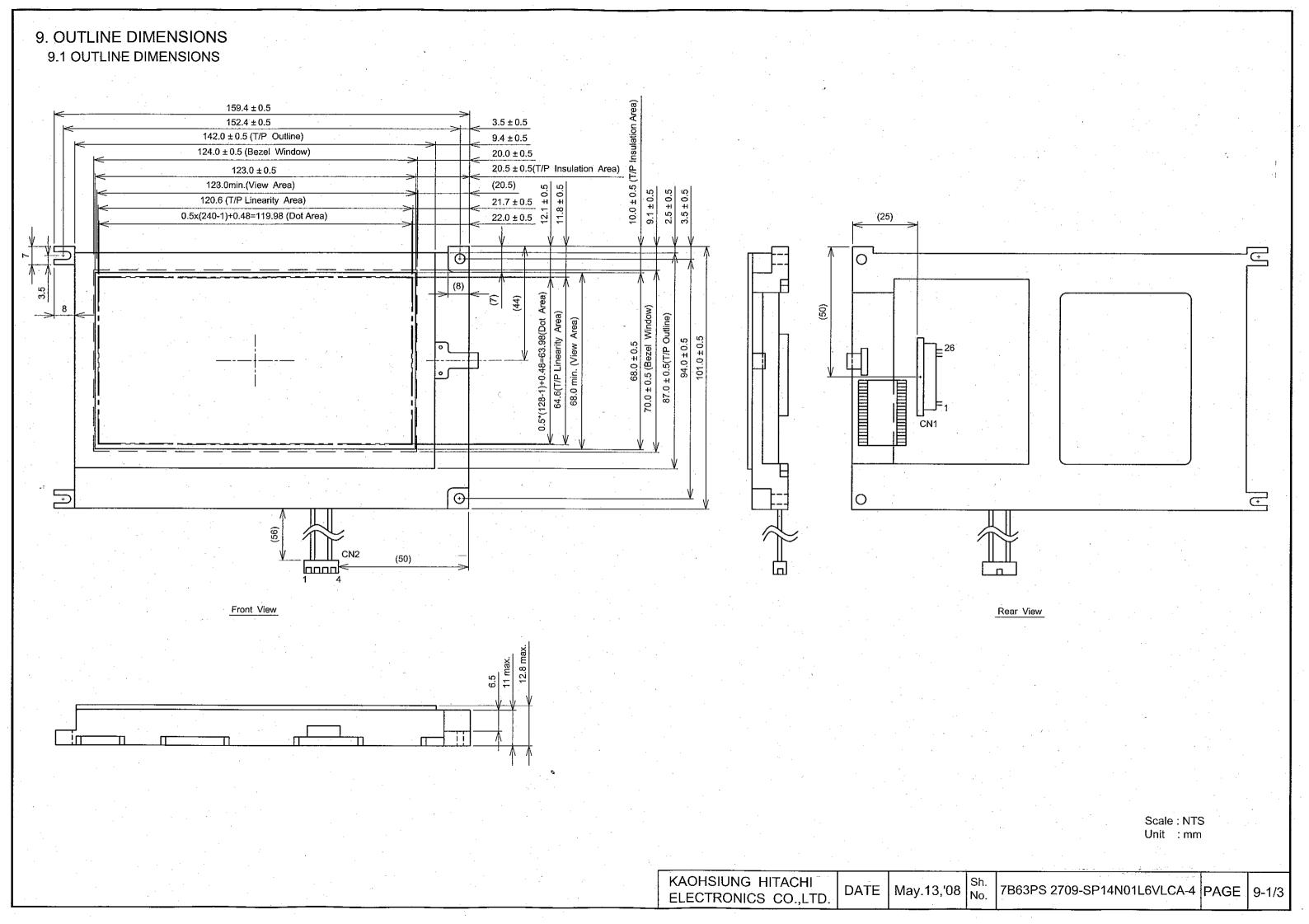
## 8.2 TIMING OF POWER SUPPLY AND INTERFACE SIGNAL



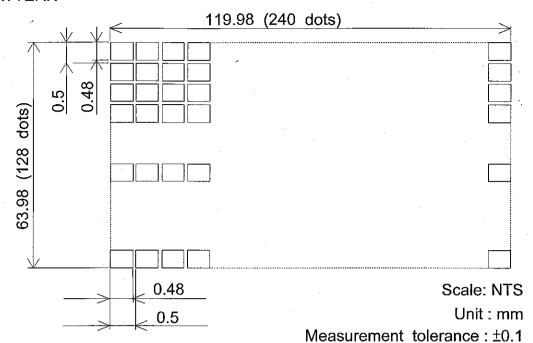
## 8.3 POWER SUPPLY FOR LCM



KAOHSIUNG HITACHI	 	M 40 100	Sh.		DAGE	0.0/0
ELECTRONICS CO.,LTD.	DATE	May.13, 08	No.	7B64PS 2708-SP14N01L6VLCA-4	PAGE	8-2/2



## 9.2 DISPLAY PATTERN



## 9.3 INTERNAL PIN CONNECTION

CN1 : Pitch 1.0mm 26pins connector Suitable connector (Molex : 52207-2690)

PIN No.	SYMBOL	FUNCTION
1	VSS	GND
2	VDD	Power Supply for Logic
	) (O( f)	No Connection . It is a test pin for reference setting
3	V0(ref)	resistant between V <sub>VR1</sub> and V <sub>VR2</sub>
		WR="L": C/D="H" Command Write
1	C/D	C/D="L" Data Write
4	- C/D	RD="L": C/D="H" Status Read
		C/D="L" Data Rwad
5	WR	Data Write (Data Write at "L")
6	RD	Data Read (Read Data at "L")
7	DB0	
. 8	DB1	
9	DB2	
10	DB3	Data Bus
11	DB4	Data Bus
12	DB5	
13	DB6	
14	DB7	
15	CE	Chip Enable (CE must be "L")
16	RET	Reset
17	NC	No Connection
18	DOFF	VDD/Display on , GND/Display off
19	F/S	Character Font Select: F/S="H" 6*8Font
19	F/3	F/S="L" 8*8Font
20	P/N	Display Mode Reverse.
21	$R_{VR1}$	For Adjusting LC Driving Voltage
22	Ŕ <sub>VR2</sub>	To Adjusting LC Driving Voltage
23	Y2	Analog signal digitizer bottom
24	X1	Analog signal digitizer right
25	Y1	Analog signal digitizer upper
26	X2	Analog signal digitizer left

CN2: JAE IL-G-4S-S3C2-SA

PIN No.	SYMBOL	FUNCTION
1	VLED -	GND
2	NC	No Connection
3	NC	No Connection
4	VLED +	Power Supply for LED

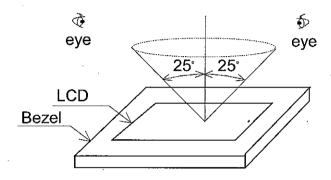
KAOHSIUNG HITACHI	D 4 TF	M 12 '00	Sh.	770470 0700 07444044 074 6744	DAGE	0.070
ELECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B64PS 2709-SP14N01L6VLCA-4	PAGE	9-3/3

## 10. APPEARANCE STANDARD

## 10.1 APPEARANCE INSPECTION CONDITION

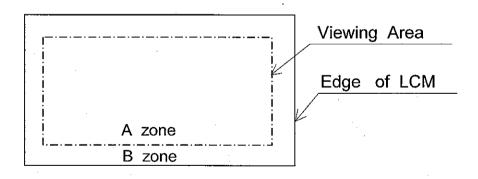
Visual inspection should be done under the following condition.

- (1) The inspection should be done under in the dark room.
- (2) The CFL should be lighted with the prescribed inverter.
- (3) The distance between eyes of an inspector and the LCD module is 25cm.
- (4) The viewing zone is shown the figure . Viewing angle  $\leq 25^{\circ}$



#### 10.2 DEFINITION OF EACH ZONE

A zone: Within the Viewing Area specified at page 9-1/3 of this document. B zone: Area between the Edge of LCM and the Viewing Area specified at page 9-1/3 of this document.



KAOHSIUNG HITACHI			Sh.		D. 0	40.440
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## 10.3 APPEARANCE SPECIFICATION

\*) If a problem occurs in respect to any of these items, both parties(Customer and HITACHI) will discuss in more detail

No.	ITEM		CRIT	ERIA			Α	В
	Scratches	Distinguished o	ne is not ac	ceptable			*	-
		(To be judged	by HITACHI	limit sar	nple)			
	Dent						*	-
	Wrinkles in Polarizer	Same as above	e				*	-
	Bubbles	Average [	Diameter	Max	ximum	Number		
	·	D(mi	m)		Accep	table		
		D∶	≦0.2		lgno	ore		
		0.2 <d< td=""><td>≦0.3</td><td></td><td>12</td><td>2</td><td>0</td><td>  -</td></d<>	≦0.3		12	2	0	-
		0.3 <d< td=""><td><b>≦</b>0.5</td><td></td><td>3</td><td>table ore 2 ne num Number cceptable Ignore 6 1 finimum Space - 10mm - Number table ore</td><td>1</td><td></td></d<>	<b>≦</b> 0.5		3	table ore 2 ne num Number cceptable Ignore 6 1 finimum Space - 10mm - Number table ore	1	
		0.5 <d< td=""><td>  **Distinguished one is not acceptable (To be judged by HITACHI limit sample)   **   Same as above</td><td></td></d<>	**Distinguished one is not acceptable (To be judged by HITACHI limit sample)   **   Same as above					
	Stains,		Filame	entous				
	Foreign	Length	Widti	า	Maxin	num Number	0	-
	Materials,	L(mm)	W(mn	n)	A	cceptable		
ŀ	Dark Spot	L≦2.0	W≤	0.03		Ignore		
		L≦3.0	0.03 <w≤< td=""><td>0.05</td><td></td><td>6</td><td></td><td></td></w≤<>	0.05		6		
L		L≦2.5	0.05 <w≤< td=""><td>0.1</td><td></td><td>1</td><td></td><td></td></w≤<>	0.1		1		
			Roi	und				
		Average Diamete	r Maximum N					
С				ıble	Space			
		D<0.2	Ignor	e		•	О	-
		$0.2 \le D < 0.33$	8			10mm		
		0.33≦D	None	)		_		
D		Total	Filamentous	s + Round	d = 10			
		Those wiped o	ut easily are	acceptal	ble		О	0
	Color Tone	To be judged	by HITACHI	limit sam	ple	,	О	-
	Color Uniformity	Same as above	•		-		О	_
	Pinhole	· · · · · · · · · · · · · · · · · · ·	•	· Max	kimum	Number		
					Accep	table		
	,	D <u>≤</u> 0	).15					
					10	)		
							]	
	Contrast	,	Contrast				0	-
	Irregularity	1				Space		·
	(Spot)	<del>_ `                                   </del>						
	•	<u> </u>			re			
	·	0.25 < D≤0.35	judged by	10				
		0.35 <d≤0.5< td=""><td>HITACHI</td><td>4</td><td></td><td>20mm</td><td></td><td></td></d≤0.5<>	HITACHI	4		20mm		
		0.5 < D		None	e			

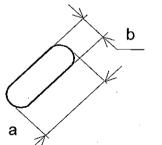
KAOHSIUNG HITACHI		May: 42 '00	Sh.				40.0/2
ELECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B64PS 2710-SP14N01L6VLCA	4	PAGE	10-2/3

No.	ITEM		CRIT	ERIA		Α	В
,	Contrast Irregularity (Line)	Width W(mm)	Length L(mm)	Maximum Number Acceptable	Minimum Space		
L	(Filamentous)	W≦0.25	L≦1.2	2	20mm	٦	
С		W≦0.2	L≦1.5	3	20mm	0	-
D		W≦0.15	L≦2.0	3	20mm	7	
		W≦0.1	L≦3.0	4	20mm	7	
		То	Total 6		7		
	Rubbing Scratch	To be judged	by HITACHI st	andard		. 0	-

No.	ITEM		IA .	
L	Dark Spots, White Spots	Average Dian	neter D(mm)	Maximum Number Acceptable
,E	Foreign Materials (Spot)	D≦	0.4	Ignore
D		D>	0.4	None
В		Width W(mm)	Length L(mm)	Maximum Number acceptable
/	Foreign Materials (Line)	W≦0.2	L<2.5	≦1
L		W ≦0.2	L>2.5	None
		W>0.2	-	None
		Width W(mm)	Length L(mm)	Maximum Number Acceptable
	Scratches	W≦0.1	-	Ignore
	Scratches	0.1 <w≦0.2< td=""><td>L≦11.0</td><td><u>≤</u>1</td></w≦0.2<>	L≦11.0	<u>≤</u> 1
		0.1 <w≦0.2< td=""><td>L≧11.0</td><td>None</td></w≦0.2<>	L≧11.0	None
		W>0.2		None

## Note

(1) Definition of average diameter D

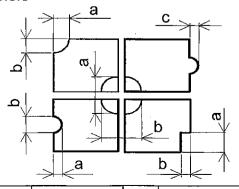


$$D = \frac{a+b}{2}$$

(2) Definition of length L and width W



(3) Definition of pinhole



C: Salience

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#### 11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VEE) AND VIEWING ANGLE RANGE.

Setting VEE out of the recommended condition will be a cause for a change of viewing angle range.

#### 11.2 CAUTION AGAINST STATIC CHARGE

As this module is provided with C-MOS LSI, the care to take such a precaution as grounding the operator's body is required when handling it.

#### 11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage.

If above sequence is not kept, C-MOS LSI of LCD modules may be damaged due to latch up problem.

#### 11.4 PACKAGING

(1) No leaving product is preferable in the place of high humidity for a long period of time.

For their storage in the place where temperature is  $35^{\circ}$ C or higher, special care to prevent them from high humidity is required.

A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer peel-off.

Please keep the temperature and humidity within the specified range for use and storage.

- (2) Since upper/bottom polarizers tend to be easily damaged, they should be handled full with care so as not to get them touched, pushed or rubbed.
- (3) As the adhesives used for adhering upper/bottom polarizers are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol.

The following solvents are recommended for use: normal hexane

please contact us when it is necessary for you to use chemicals.

(4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois, soaked in the chemicals recommended without scrubbing it hardly.

To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.

- (5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.
- (6) Foggy dew deposited on the surface and contact terminals due to coldness will be caused for polarizer damage, stain and dirt on product.

When necessary to take out the products from some place at low temperature for test, etc.

It is required for them to be warmed up in a container once at the temperature higher than that of room.

(7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands.

(Some cosmetics are detrimental to polarizers.)

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(8) In general the quality of glass is fragile so that it tends to be cracked or chipped in handling, specially on its periphery.

Be careful not to give it sharp shock caused by dropping down, etc.

#### 11.5 CAUTION FOR OPERATION

- (1) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life.
  - An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current driver should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark blue color in them.
  - However those phenomena do not mean malfunction or out of order with LCD's which will come back in the specified operating temperature range.
- (3) IF the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- (4) A slight dew depositing on terminals is a cause for electrochemical reaction resulting in terminal open circuit.
  - Usage under the relative condition of 40 ℃ 50%RH or less is required.
- (5) Prevent continuous 4 hours or over same pattern displaying, to avoid Image-Sticking.

#### 11.6 STORAGE

- In case of storing for a long period of time (for instance, for years) for the purpose of replacement use, the following ways area recommended.
- (1) Storage in a polyethylene bag with the opening sealed, so the fresh air will not be entered from outside.
- (2) Placing in a dark place where neither exposure to direct sunlight nor light is , keeping temperature in the range from  $0^{\circ}$ C to  $35^{\circ}$ C.
- (3) Storing with no touch on polarizer surface by anything else.

  (It is not recommended to store them as they have been contained in the inner container at the time of delivery from us.)

#### 11.7 SAFETY

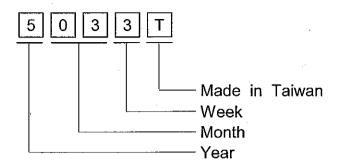
- (1) It is recommendable to crash damage or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- (2) When any liquid leaked out of a damage glass call comes in contact with your hands, please wash it off well with soap and water.

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## 12. DESIGNATION OF LOT MARK

12.1 Lot mark

Lot mark is consisted of 4 digital number.



VEAD	FIGURE IN
YEAR	LOT MARK
2008	8
2009	9
2010	0
2011	1
2012	2

Note 1: Some products have alphabet at the end or the first.

MONTH	FIGURE IN LOT MARK	MONTH	FIGURE IN LOT MARK
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

WEEK	FIGURE
(DAY IN	INLOT
CALENDAR)	MARK
1~7	1
8~14	2
15~21	3
22~28	4
29~31	5

#### 12.2 REVISION

REV No.	ITEM	LOT No.	
	CFL I/F Connector:		
-	Mitsumi M63M83 - 04	-	
^	1.CFL I/F Connector :JAE IL-G-4S-S3C2-SA	7400T	
A	2.Operating Life (40,000h)	7102T	

## 12.3 LOCATION OF LOT MARK on the back side of LCM

5033T

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#### 13 PRECAUTION FOR USE

- 13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity.
  Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- 13.2 On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
  - (1) When a question is arisen in the specifications.
  - (2) When a new problem is arisen which is not specified in this specifications.
  - (3) When an inspection specifications change or operating condition change in customer is reported to HITACHI, and some problem is arisen in this specification due to the change.
  - (4) When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any request, please contact HITACHI.

#### 14. TOUCH PANEL SPECIFICATION

#### 14.1 RATINGS

#### 14.1.1 ABSOLUTE MAXIMUM RATINGS

ITEM	SPECIFICATION	COMMENT
		COMMITTER
Operating Voltage	(7V)	•
Contact Current	(20mA)	Without
Operating Temperature	(0~55°C 20~85%RH)	Condensation
Storage Temperature	(-20~70°C 20~85%RH)	

#### 14.1.2 OPERATING CONDITIONS

	<del>-</del>	
ITEM	SPECIFICATION	
Operating Voltage	5VDC	
Contact Current	10 ~ 20 mA	
Actuation Force	1.2N max.	

#### 14.2 MECHANICAL STRENGTH

#### 14.2.1 INPUT METHOD & ACTUATION FORCE

INPUT METHOD	ACTUATION FORCE	COMMENT		
PEN	1.2N max.	R0.8, Polyacetal pen		

## 14.2.2 SURFACE HARDNESS (2h min.)

#### 14.3 OPTICAL CHARACTERISTICS

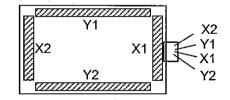
14.3.1 TRANSPARENCY: (76% min.)

14.3.2 HAZE: (5% max.)

#### 14.4 ELECTRICAL CHARACTISTICS

#### 14.4.1 CONDUCTIVE RESISTANCE

TERMINAL	CONDUCTIVE RESISTANCE
X1-X2	(150~1300Ω)
Y1-Y2	(150~1300Ω)



#### 14.4.2 INSULATION RESISTINCE

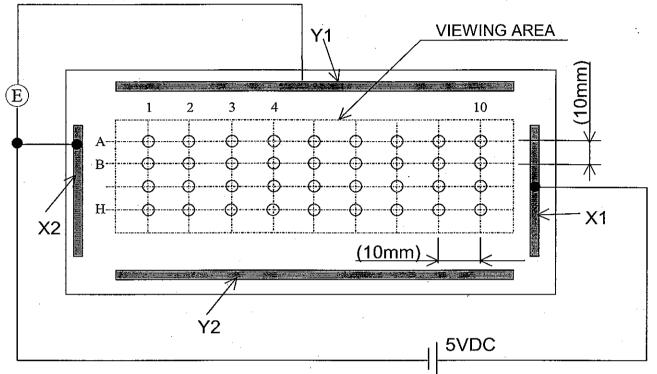
TERMINAL	INSULATION RESISTANCE	TESTING VOLTAGE
X-Y	(20MΩ)	25VDC

## 14.4.3 BOUNCE CHATTERING 10msec max.

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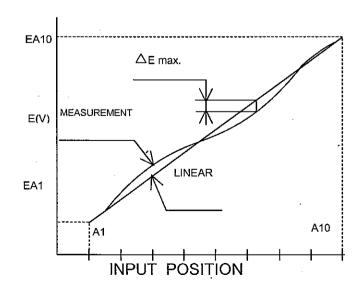
#### 14.4.4 LINEARITY

- (1) LINEARITY
  - LINEARITY DEVIATION: (2% max.)
- (2) TESTING CIRCUIT
  - (a) X AXIS LINEARITY TESTING METHOD, 100g, VX1-VX2=5V, VOUT=VY1.



- (b) Y AXIS LINEARITY METHOD VY1-Y2=5V, VOUT=VX1
- (3) CALCULATION
  - (a) X AXIS LINEARITY

LINEARITY= 
$$\frac{\triangle \text{ E max.}}{\text{E A10} - \text{E A1}} \times 100(\%)$$



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## 14.5 APPEARANCE SPECIFICATION

Description		Reject criteria						
Film dent		D > 0.3 : To be zero						
Foreign	Dot type	0.3 ≥ D > 0.2 : To be max 2points						
Material Between		interval of faults is 50mm min. $0.2 \ge D$ : None-specify						
Glass & Film		D1 D2 <u>D1+D2</u> [mm]						
	Line type	$W \ge 0.1$ : refer to "Dot type" $0.1 > W \ge 0.05$ With $L \ge 5$ : To be zero						
Scratch		0.1 > W ≥ 0.05 With L < 5 : To be max 2points interval of faults is 50mm min.						
		0.5 > W : None-specify  W : Width [mm]  L : Length [mm]						
Film dot type l Film hard-coa Missing		Area $0.5 \text{mm}^2 \leq$ : To be zero Area $0.3 \text{mm}^2 \leq$ < $0.5 \text{mm}^2$ : To be max. 5 points Area $0.3 \text{mm}^2 \leq$ : None-specify						
Glass flaw		To be no flaw which size is over the drawing specified as Below. Number of flaw is none-specify.  Traveling flaw is none.  Flaw of thickness-direction  Size is glss-thickness max.						

			_			
KAOHSIUNG HITACHI			Sh.	TROADO OTAM ODAANOALOW OA A	DACE	44 2/2
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